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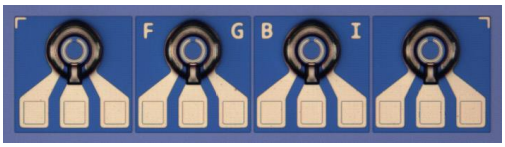
# (Preliminary ver.) 1x4 25 Gbps 850 nm PIN Array (GSG)

## L-CR-IM00-02

**Part Number: L-CR-IM00-02**
**Applications: 100 Gbps**

**Absolute Maximum Ratings (T = 25°C):**

| Parameter | Symbol | Unit | Value |
|---|---|---|---|
| Forward Current | $I_F$ | mA | 10 |
| Reverse Voltage | $V_R$ | V | 40 |
| Reverse Current | $I_R$ | mA | 1 |
| Operating Temperature | $T_{op}$ | °C | 0 - 90 |
| Storage Temperature | $T_{stg}$ | °C | -40 - 100 |




**Electro-optical Characteristics (T = 25°C, unless noted otherwise):**

| Parameter | Symbol | Unit | Min. | Typ. | Max. | Test Condition |
|---|---|---|---|---|---|---|
| Aperture | D | μm | | 38 | | |
| Responsivity | R | A/W | | 0.5 | | $V_R$ = 5 V<br>λ = 850 nm |
| Dark Current | $I_D$ | nA | | 0.1 | 1.0 | $V_R$ = 5 V |
| Breakdown Voltage | $V_B$ | V | 50 | | | $I_R$ = 1 μA |
| Capacitance | C | pF | | 0.09 | 0.14 | $V_R$ = 2 V<br>f = 1 MHz |
| Peak Wavelength | λ | nm | | 850 | 860 | |

Chip configuration:

1. Both anode and cathode contacts on top (epi) surface.
2. Dimension: 290 (length) x 1050 um (width) x 130 um (thickness) with 250um pitch
   Tolerance: +/-12.5um
   Per channel dimension: 250 um (width) x 290 um (length)
3. Bond pad size: 60 x 60 um square
4. P-bond pad in the middle